

Fig. 1. Block diagram of a System-on-Chip (SoC) architecture.

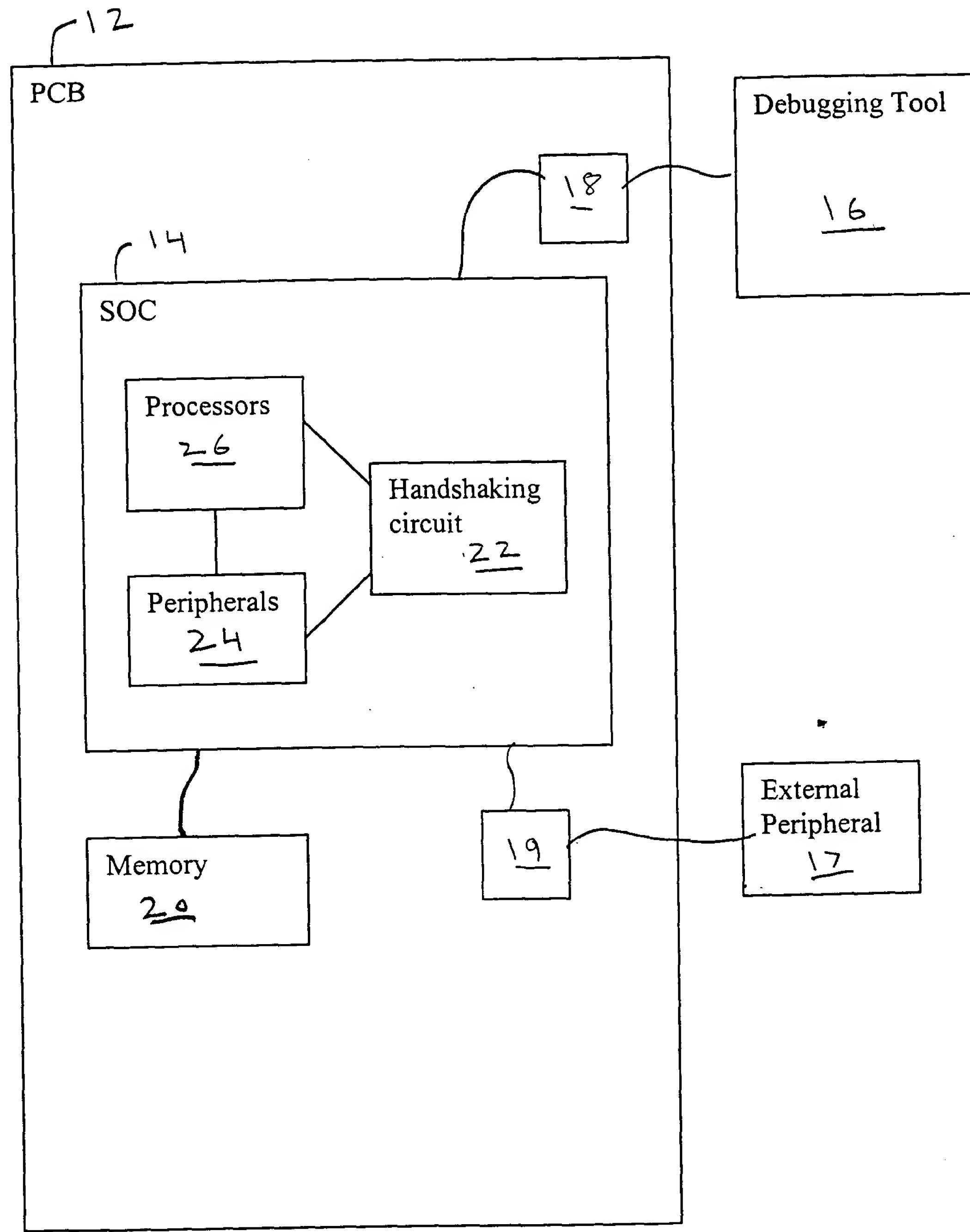


FIG. 1

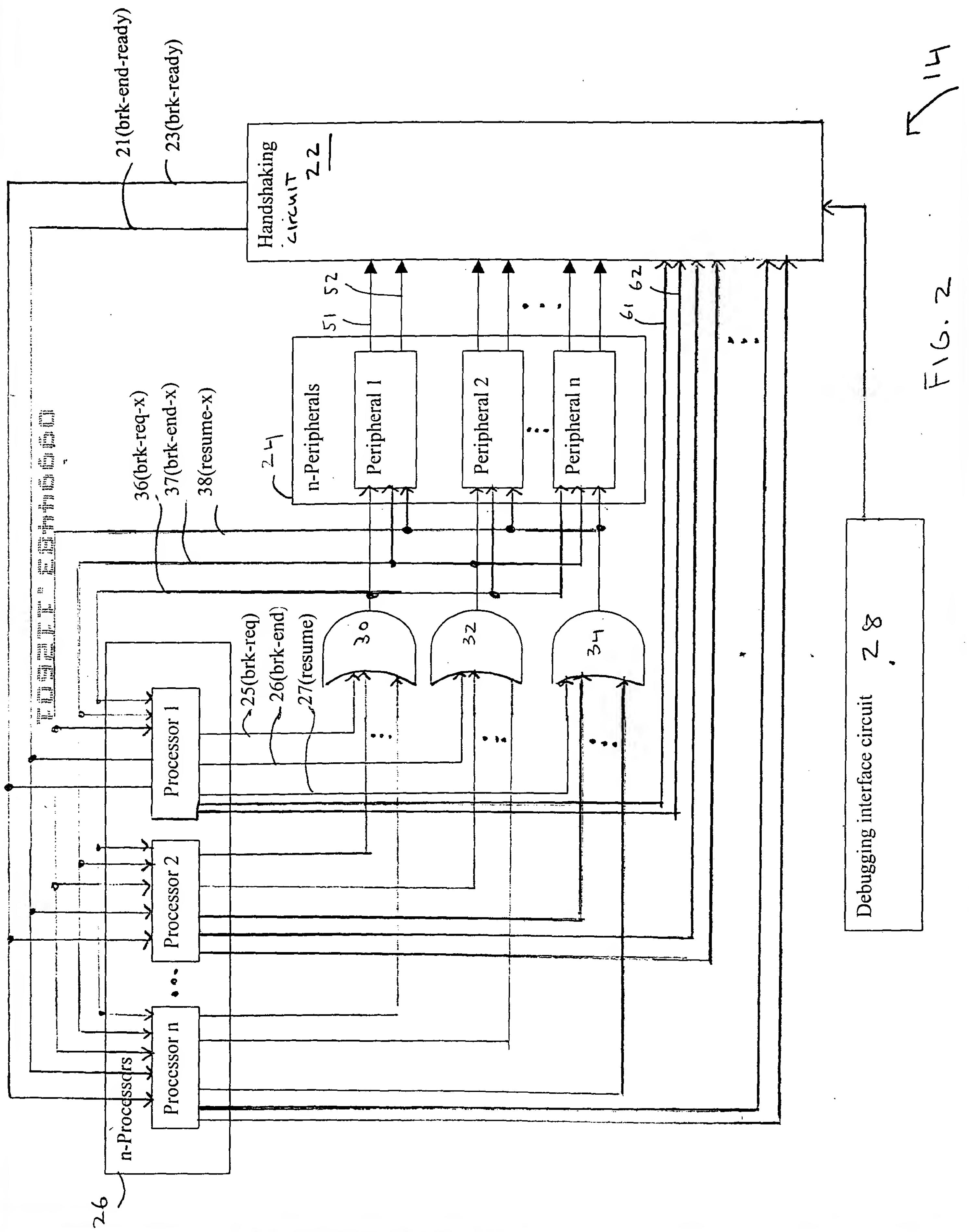
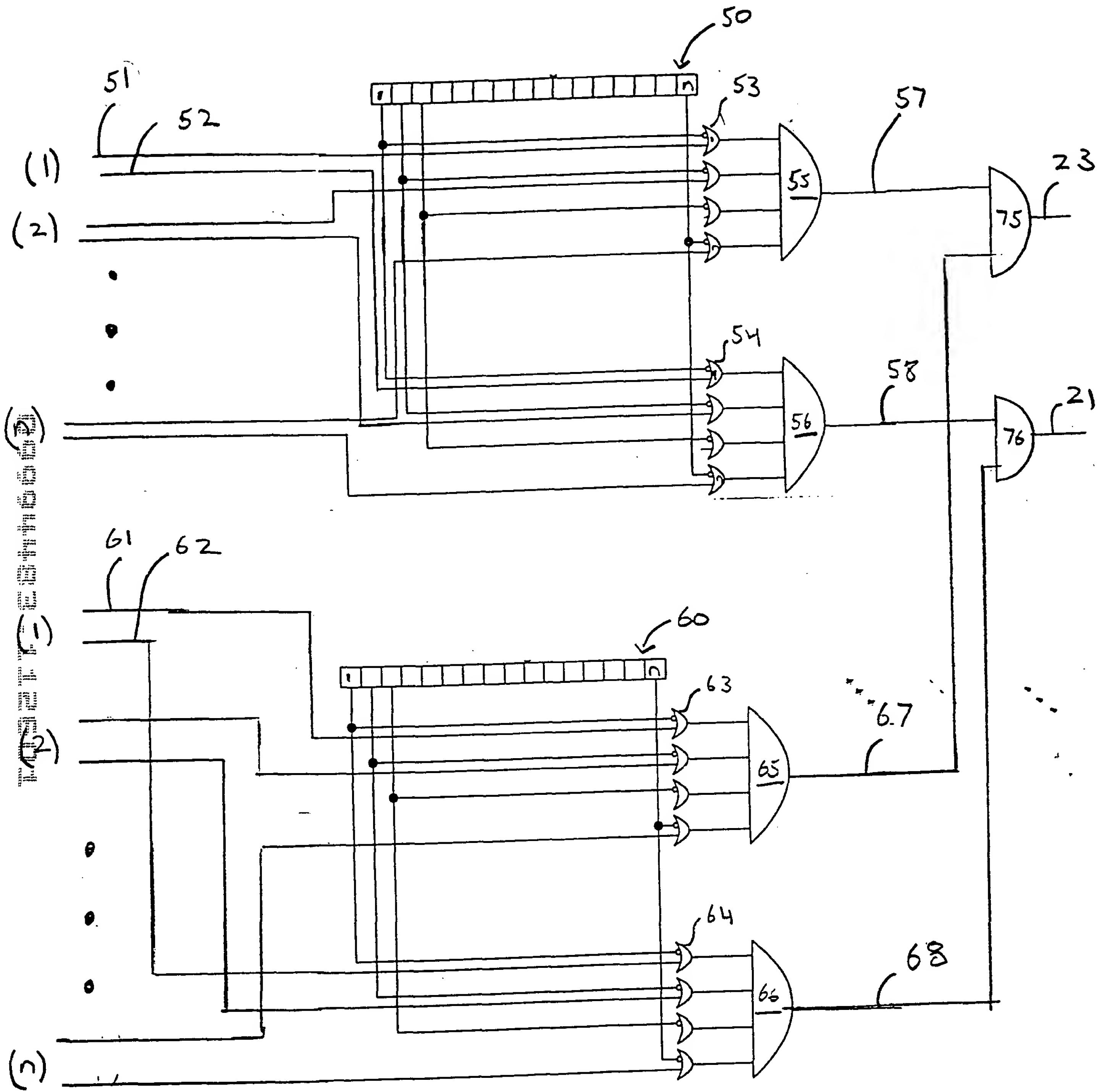


Fig. 2



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Fig. 6.3

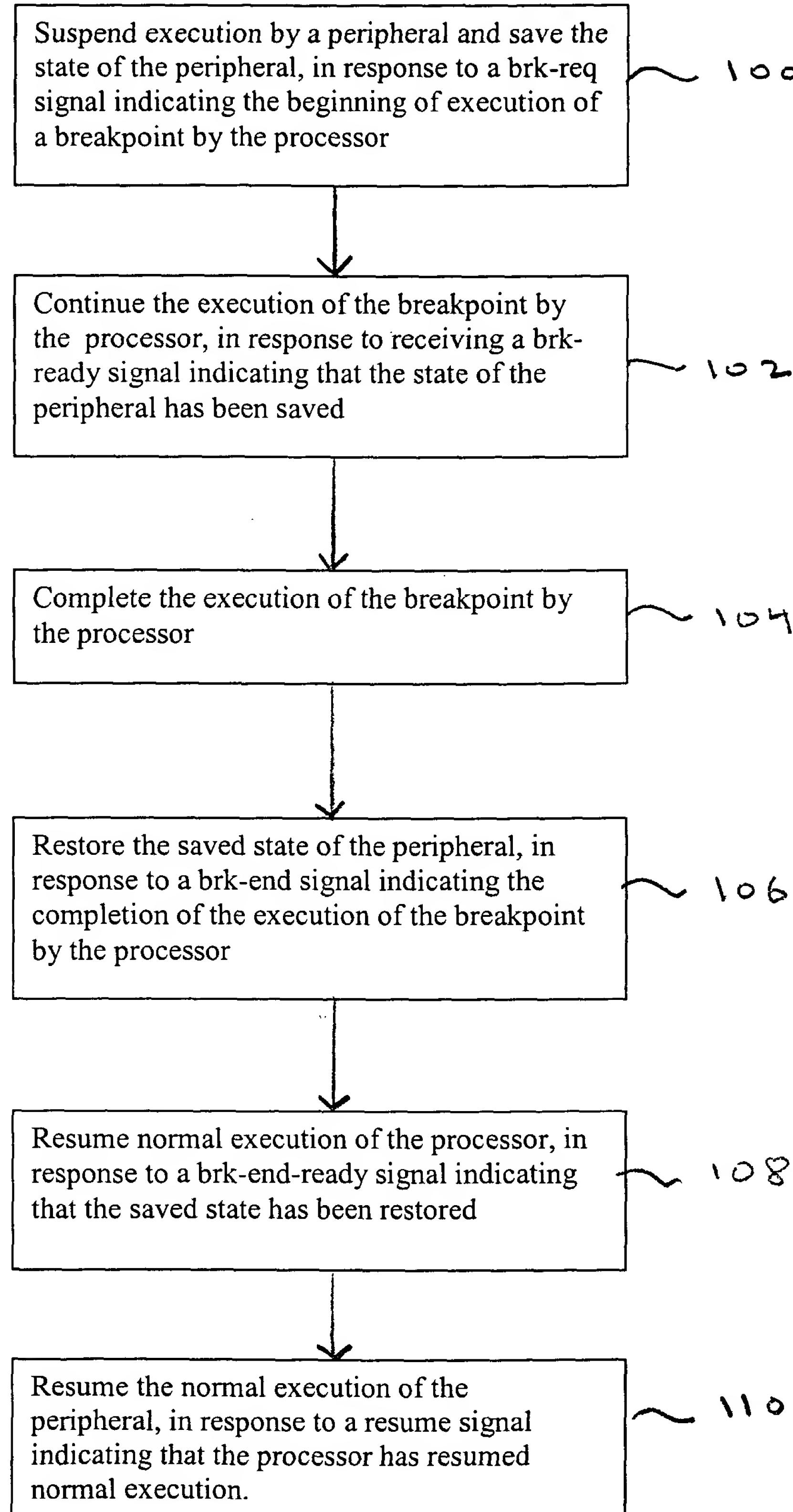


FIG. 4